



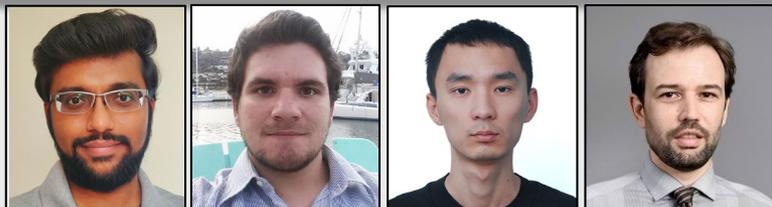
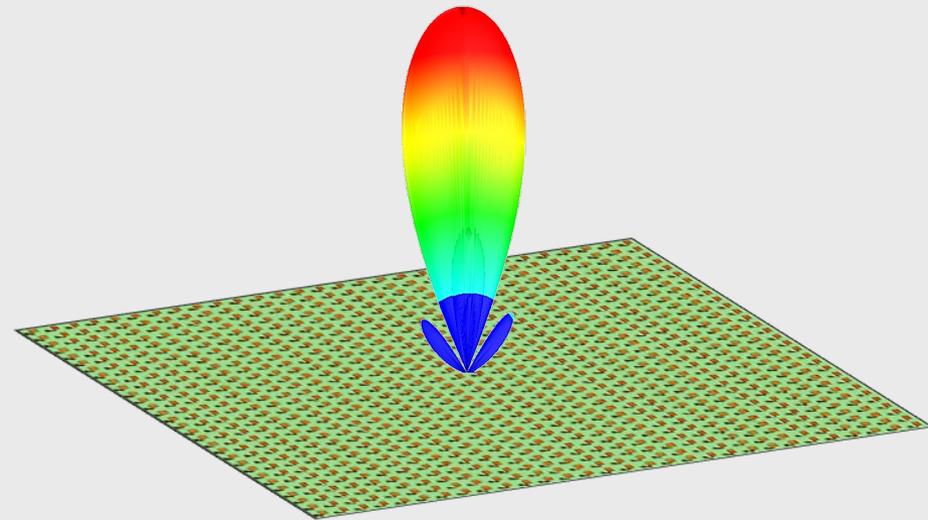
ARIZONA STATE UNIVERSITY



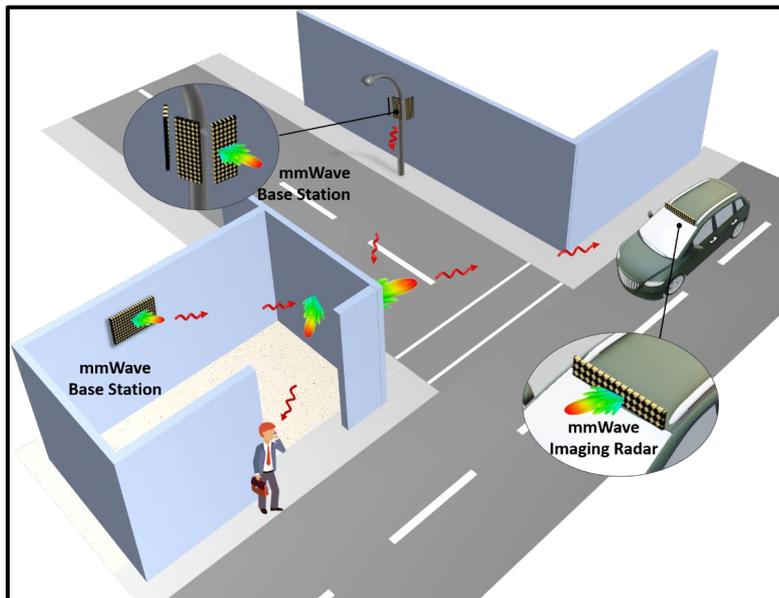
# Radar Cross Section Characterization of a 222.5 GHz Reconfigurable Reflective Surface

Bharath G. Kashyap, Panagiotis C. Theofanopoulos, Yiran Cui, and George C. Trichopoulos

Terahertz Electronics Laboratory  
Electrical, Computer, and Energy Engineering  
**Arizona State University, Tempe, AZ**  
E-mail: [Bharath.Kashyap@asu.edu](mailto:Bharath.Kashyap@asu.edu)

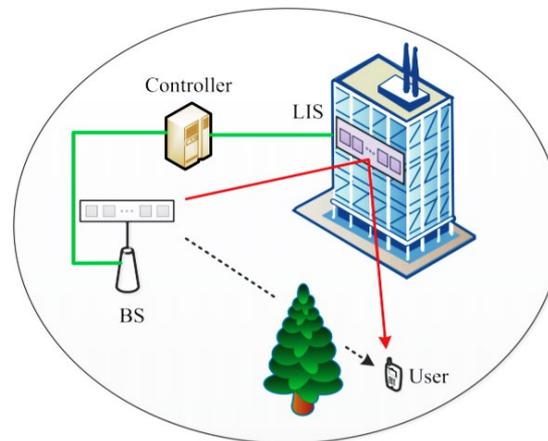


## Wireless Communications



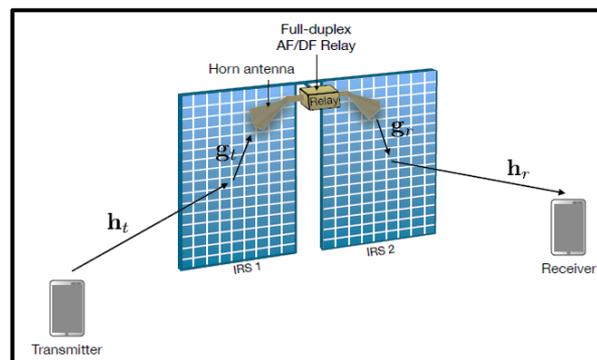
### Point-to-point relays

- Improved Coverage.
- Easily embedded systems.
- Reduce the need for more base stations (5G, 6G..) → reduced cost.
- Power efficiency.



### Large intelligent Surfaces (LISs)

Y. Han, et.al., "Large Intelligent Surface-Assisted Wireless Communication Exploiting Statistical CSI," in *IEEE Transactions on Vehicular Technology*, 2019.

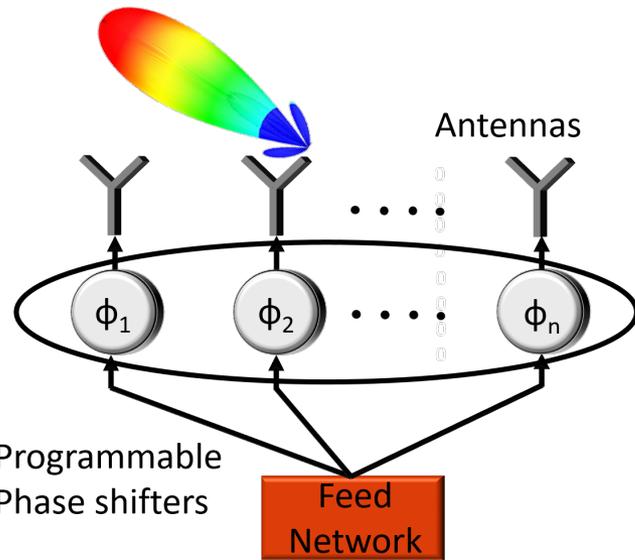


### Relay aided intelligent reconfigurable surfaces (IRSs)

X. Ying, et.al., "Relay Aided Intelligent Reconfigurable Surfaces: Achieving the Potential Without So Many Antennas," arXiv:2006.06644v1.

# Why Reflective Surfaces?

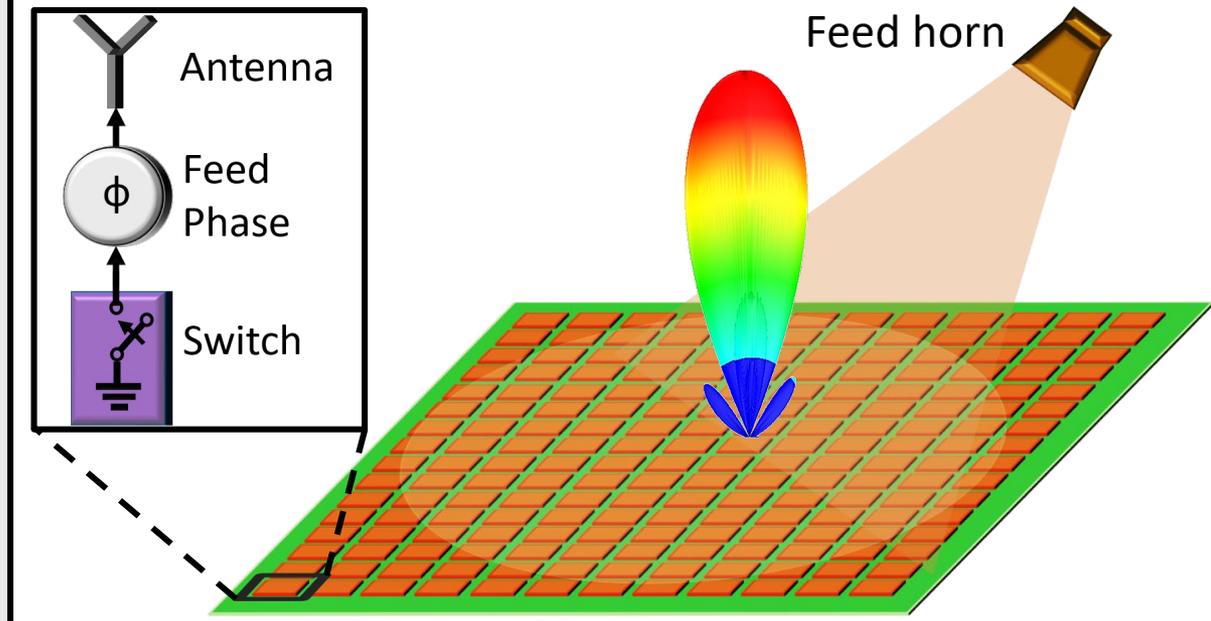
## Phased Arrays:



### Challenges

- Large hardware footprint.
- High implementation cost.
- Lossy transmission lines at mmWave/THz frequencies  
→ Low efficiency.

## Reflective Surfaces:



### Advantages

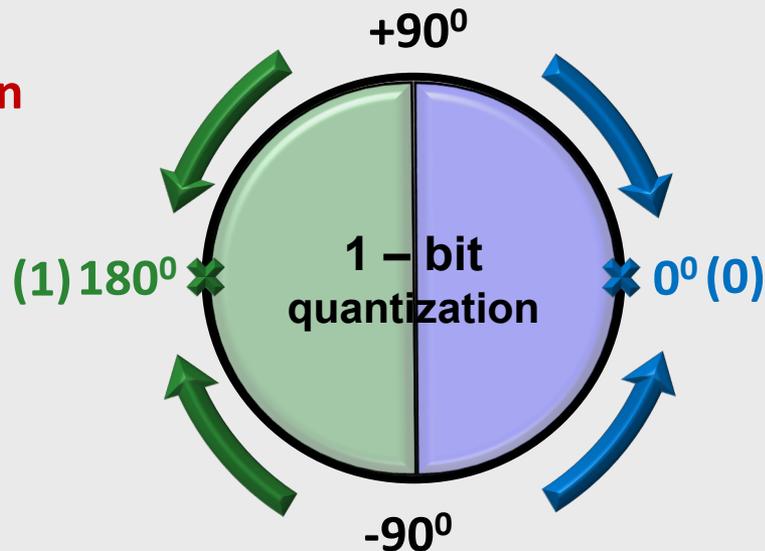
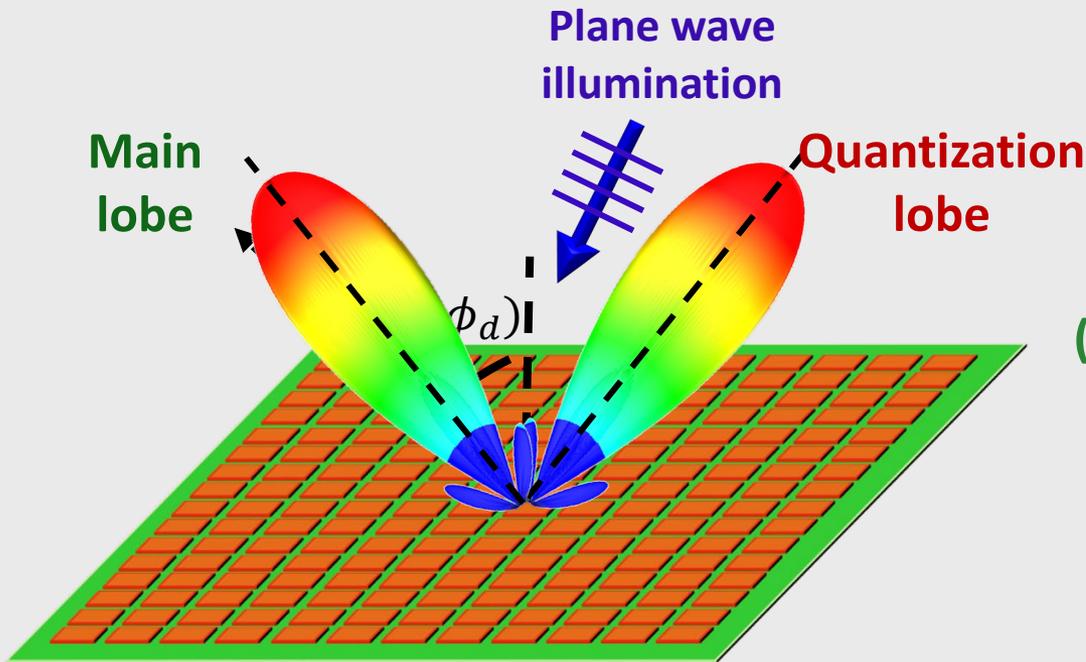
- Low profile and low weight.
- Relative ease of fabrication.
- Low implementation cost.
- Non-scalable losses.
- Compatible for high frequency designs.

### Existing Switching

#### Techniques:

- Graphene
- Vanadium-di-oxide
- HEMT

# Quantization Lobe in 1-bit Reflective Surface Designs



$$\varphi_{mn}^0 = -k_0(x_m \sin \theta_d \cos \phi_d + y_m \sin \theta_d \sin \phi_d)$$

$$\varphi_{mn}^{illum} = k_0(x_m \sin \theta_i \cos \phi_i + y_m \sin \theta_i \sin \phi_i)$$

$$\varphi_{mn} = \varphi_{mn}^0 - \varphi_{mn}^{illum}$$

## Quantization lobe results from:

- Periodicity of the phase rounding quantization error.
- Lower # of bits  $\rightarrow$  higher error.

# Mitigating Quantization Lobes

Straight forward solution:

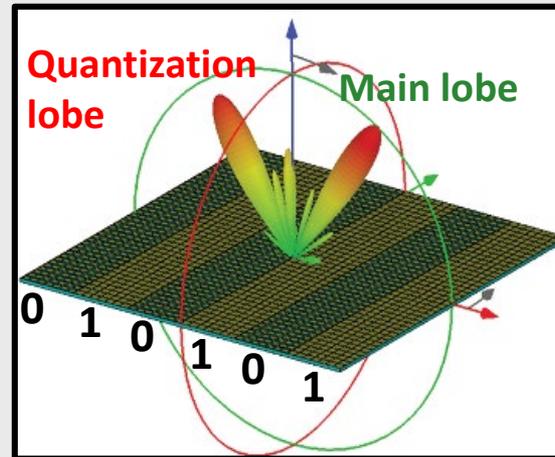
Use more bits!

E.g. 2-bit design →  
Quantization lobe level  
greatly reduced.

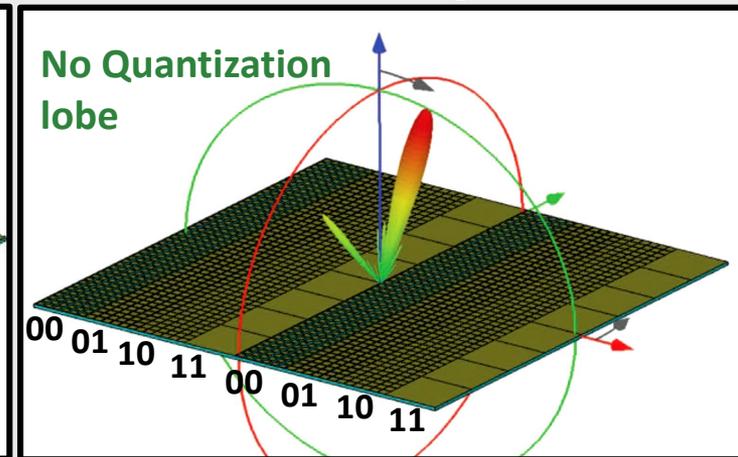
Challenges

- Bulky feed and biasing circuit.
- More RF devices/switches.

1-bit design



2-bit design



T. J. Cui, et.al., "Coding Metamaterials, Digital Metamaterials, and Programmable Metamaterials." in *Light: Science and Applications*, 2014.

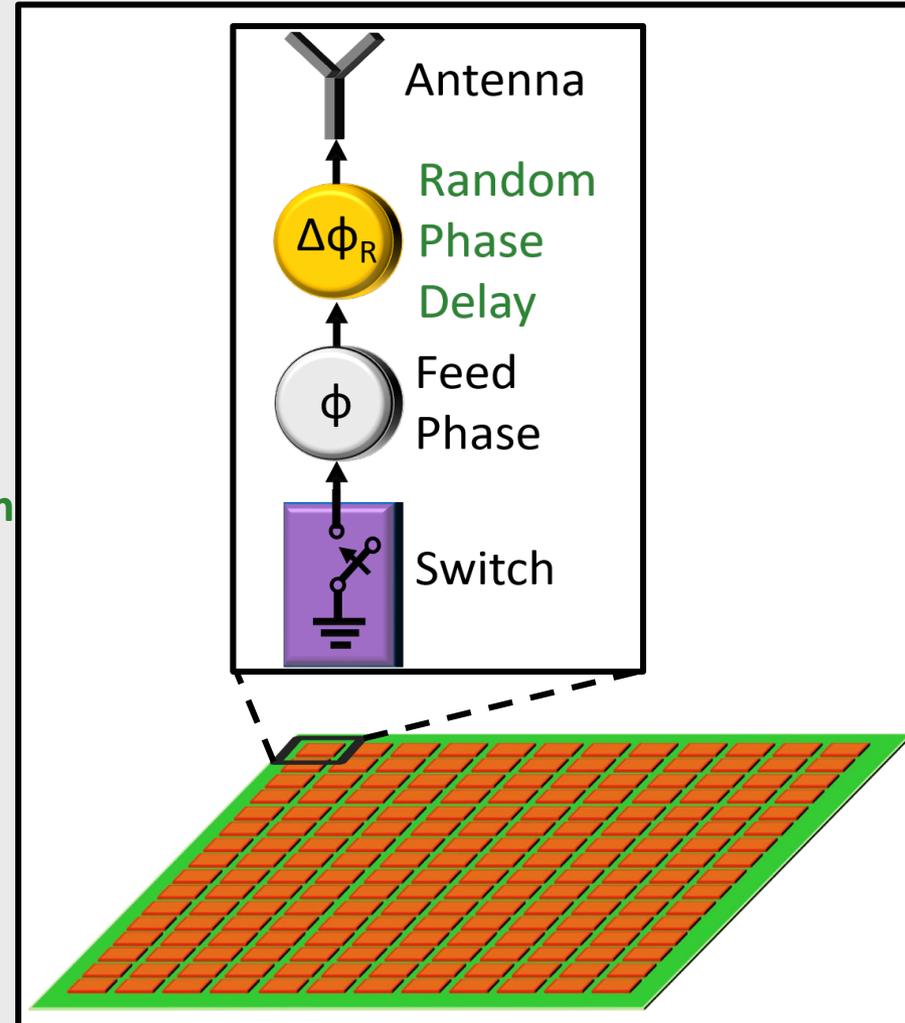
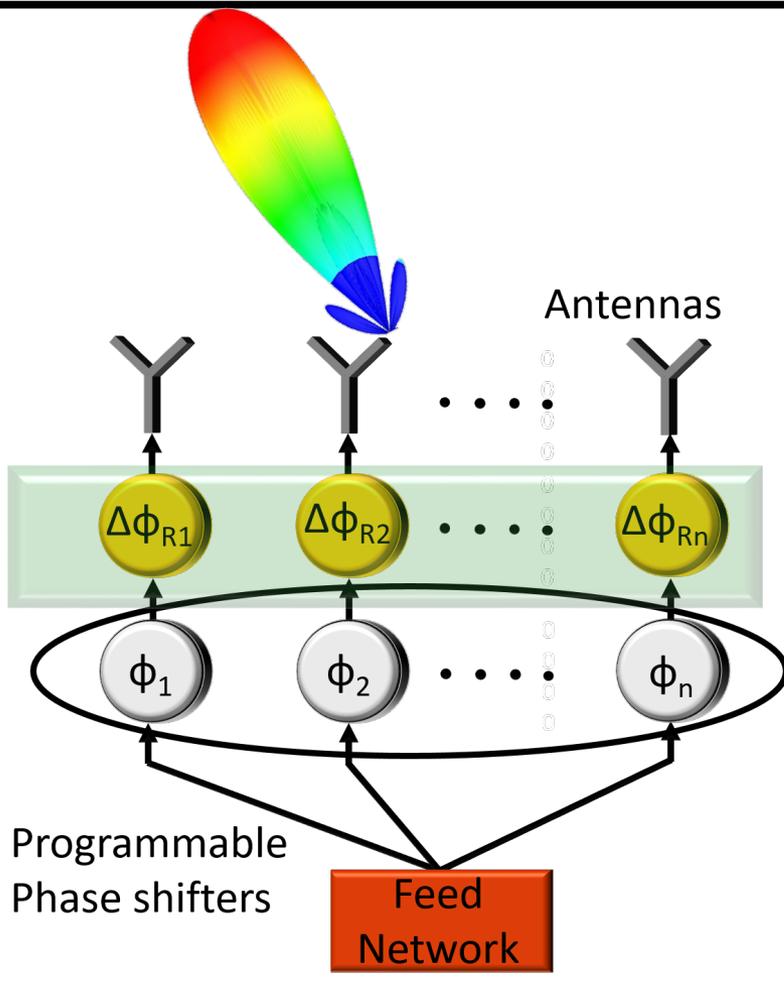
So what do we need?

A technique to suppress quantization lobes using 1-bit coding

# Random Phasing Technique to Suppress Quantization Lobes

Technique first introduced in phased arrays

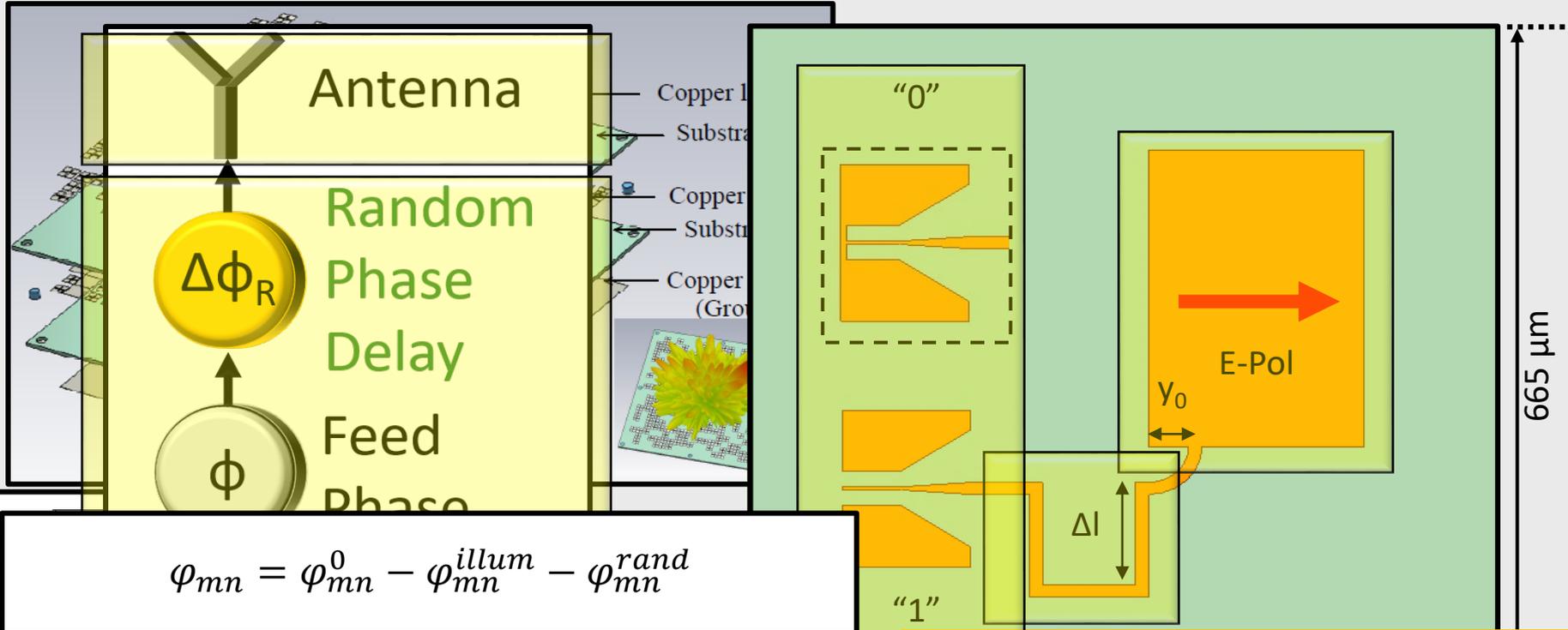
Implementation in Reflective Surfaces



# Existing Designs vs Proposed Design

Reproducible and Reproducible Proposed Design

Unit cell @ 226 GHz



$$\phi_{mn} = \phi_{mn}^0 - \phi_{mn}^{illum} - \phi_{mn}^{rand}$$

For a unit cell



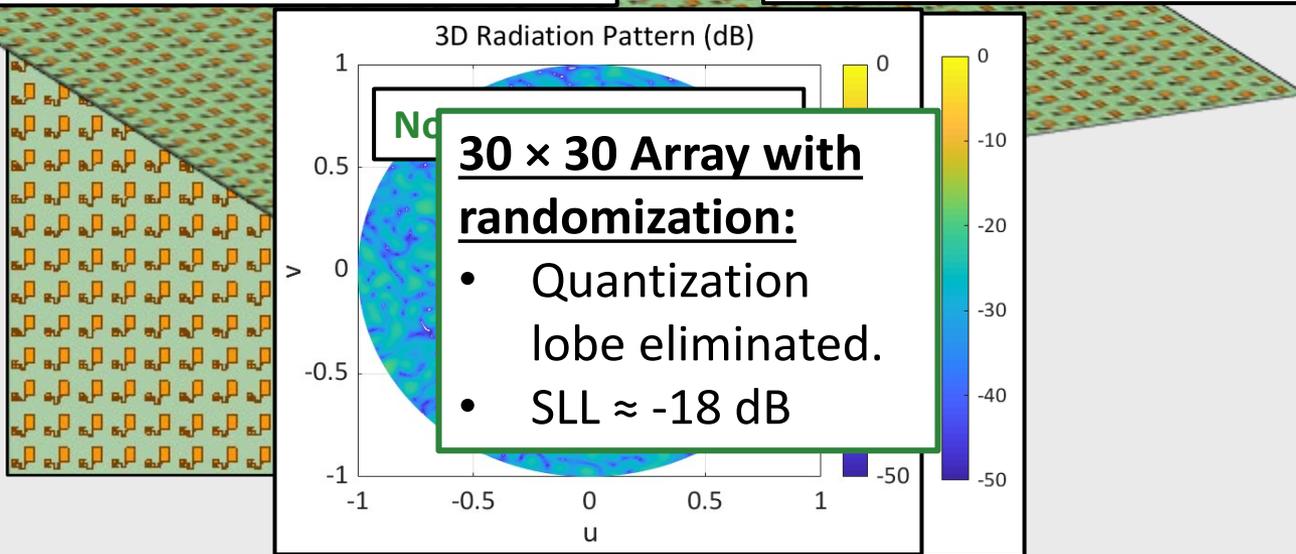
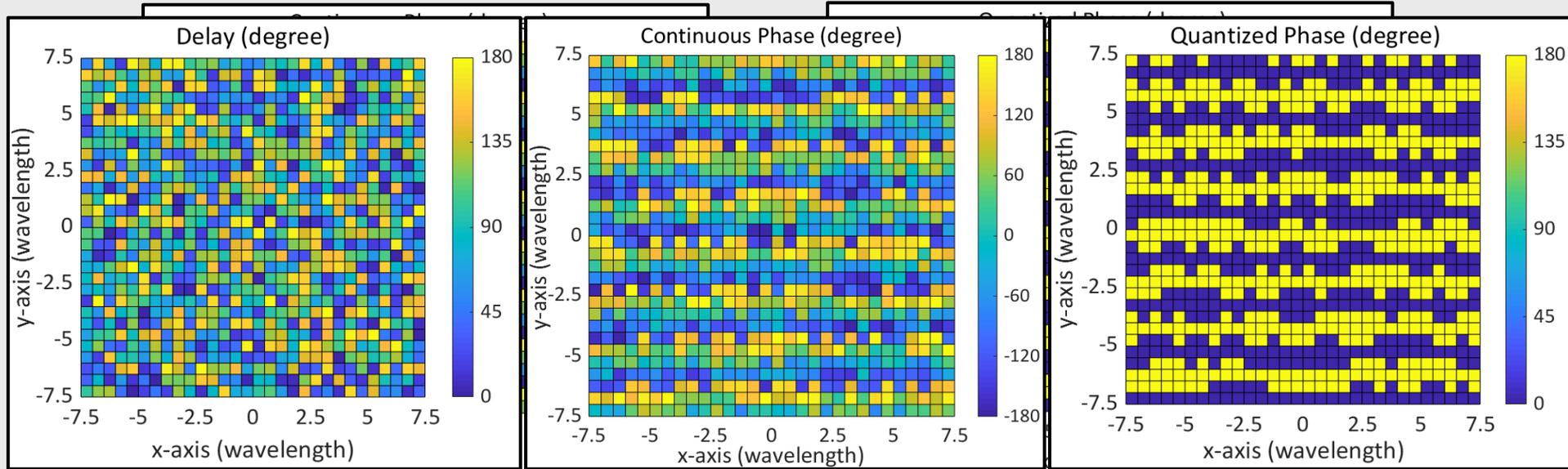
Switch losses are maintained using a generator

$$\phi_{rand} = rand(1, N) \cdot \pi$$

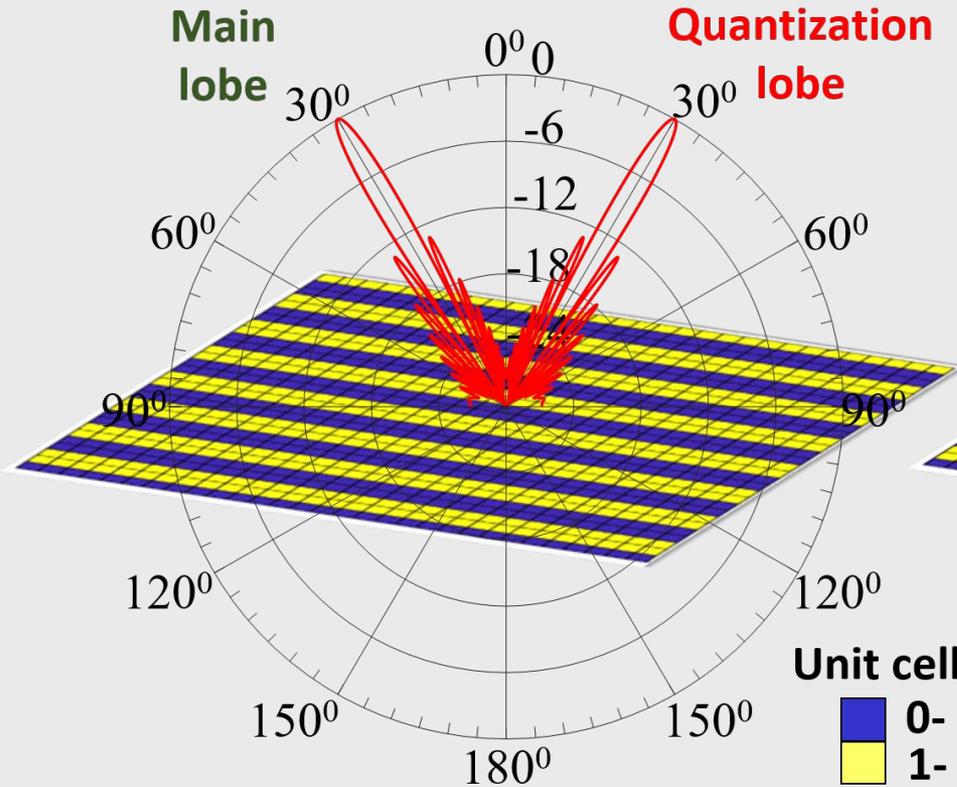
- Single layer implementation
- Random phasing is introduced through varying lengths of delay lines ( $\Delta l$ ).
- $180^\circ \pm 10^\circ$  phase shift achieved between the states '0' and '1'.

# Effect of Introducing Random Phasing

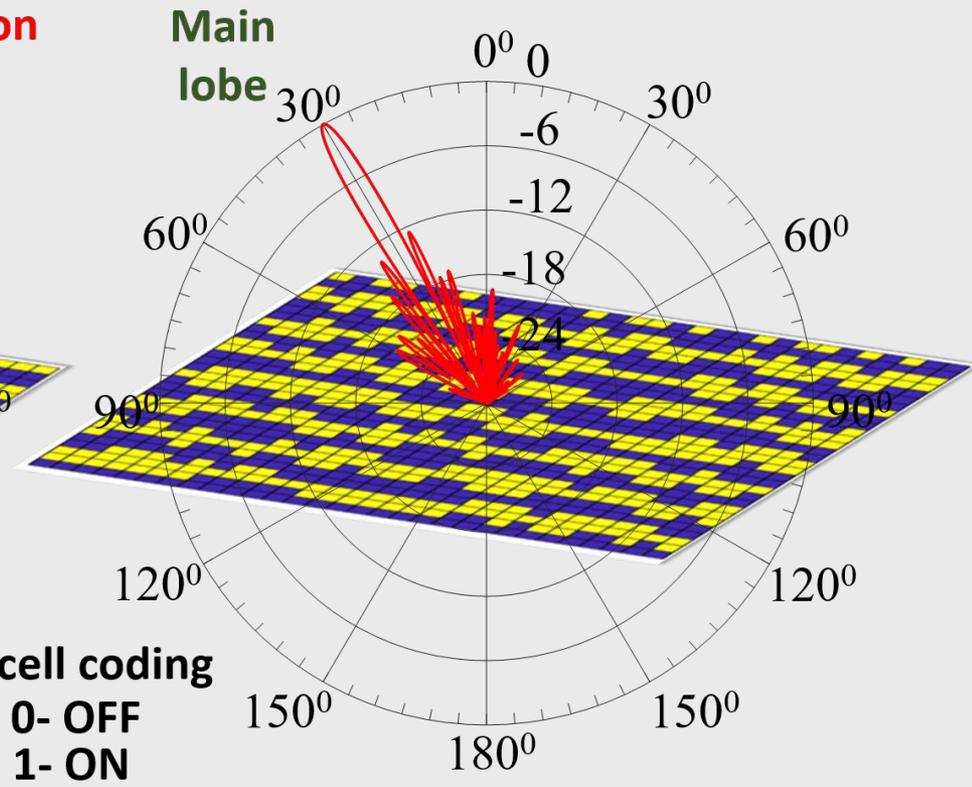
$$\varphi_{mn} = \varphi_{mn}^0 - \varphi_{mn}^{illum} - \varphi_{mn}^{rand} \rightarrow 0$$

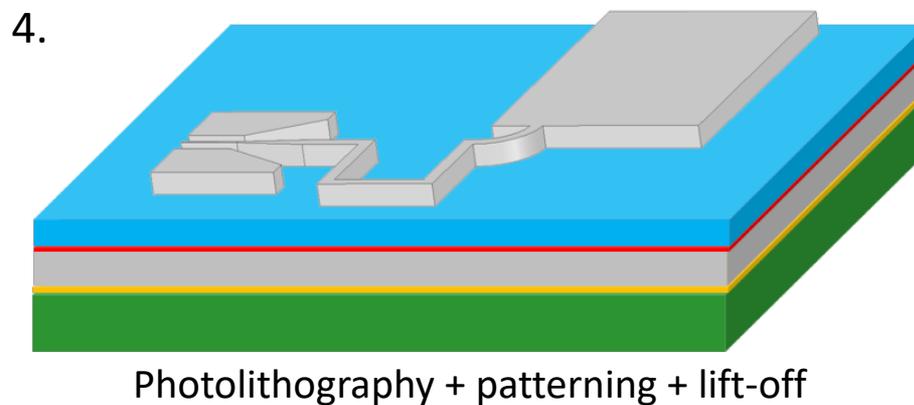
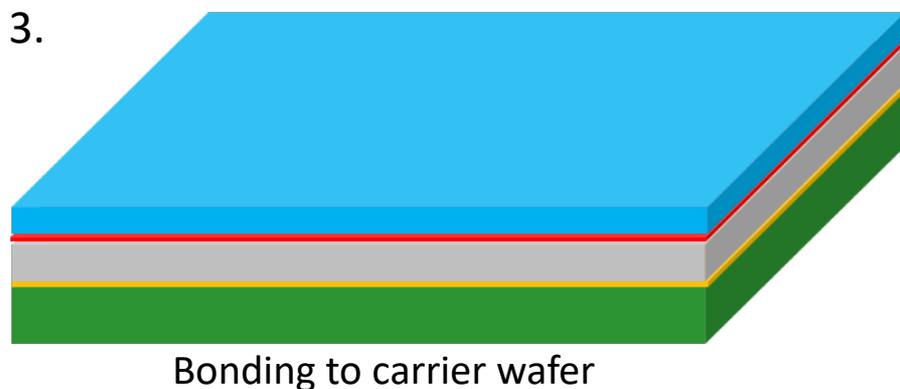
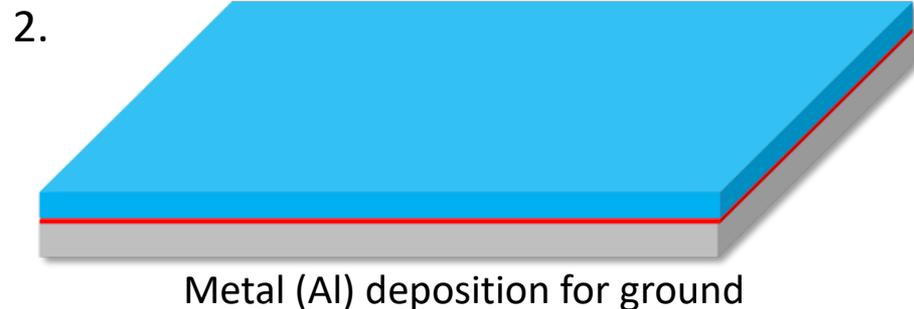


## Non-randomized RRS



## Randomized RRS

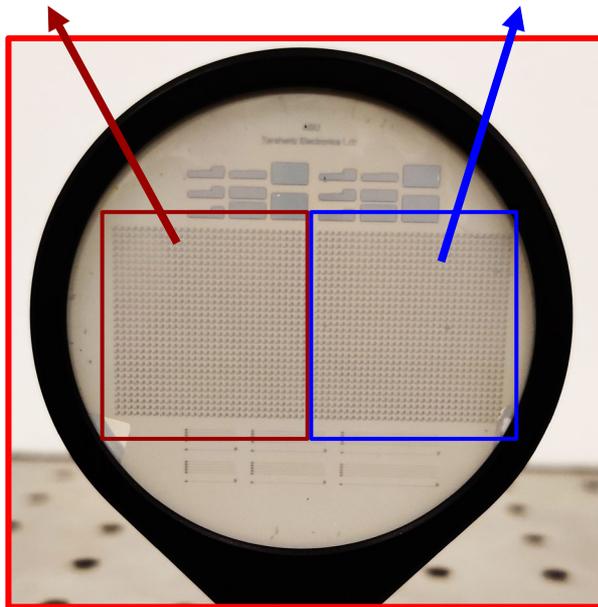




-  Alumina ribbon ceramic substrate from Corning Inc. ( $\epsilon_r = 10, \tan\delta = 10^{-3}$ )
-  Aluminum (ground + patch)
-  Bonding glue
-  Titanium
-  Carrier wafer (500  $\mu\text{m}$  quartz)

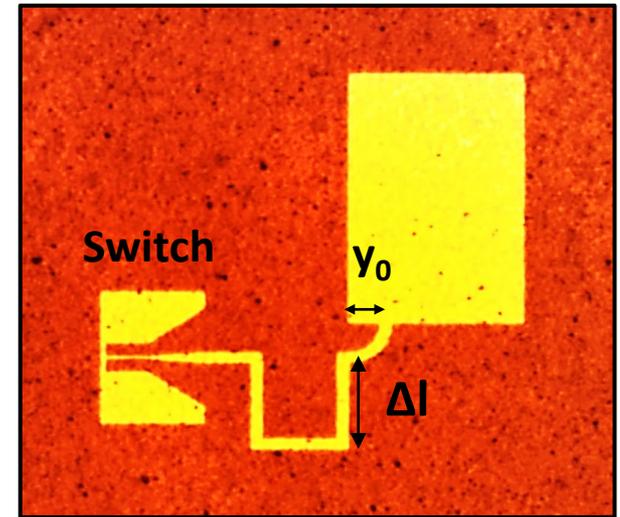
**Randomized RRS**

**Non-Randomized RRS**

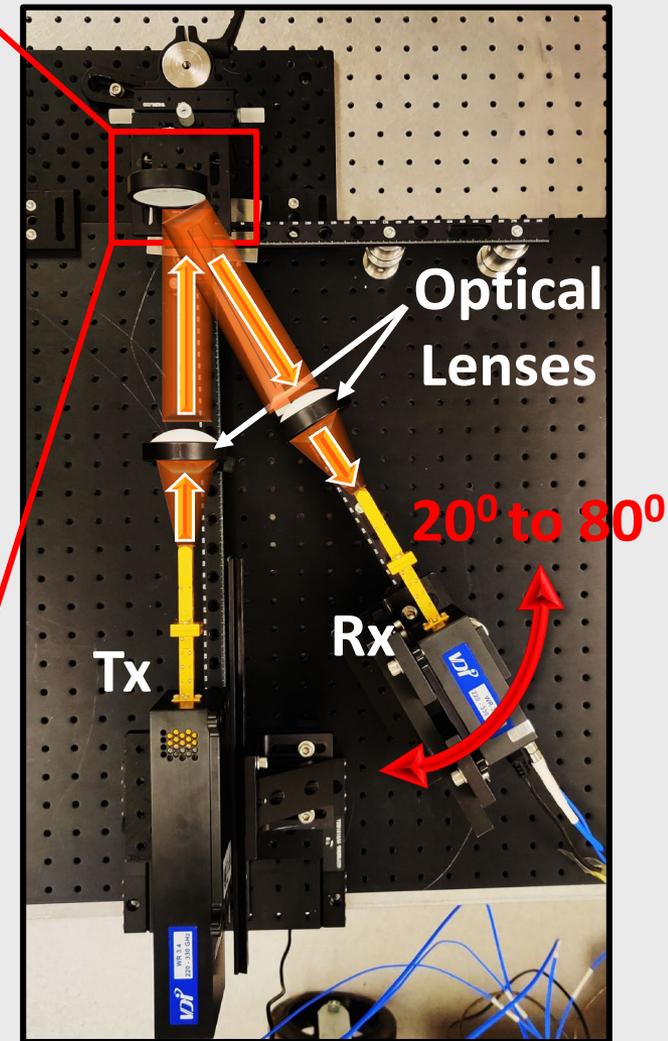
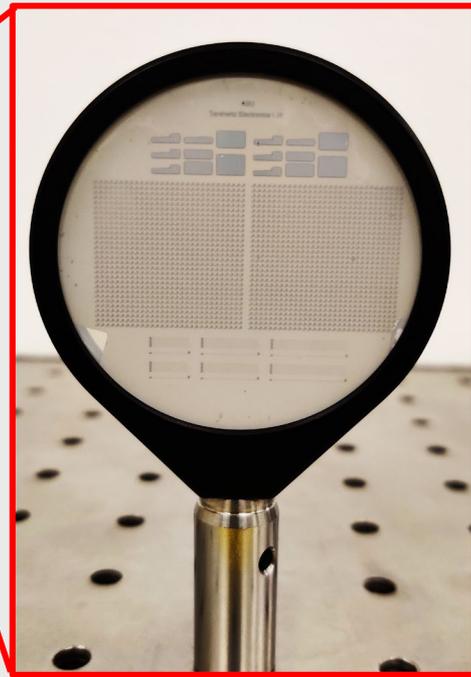
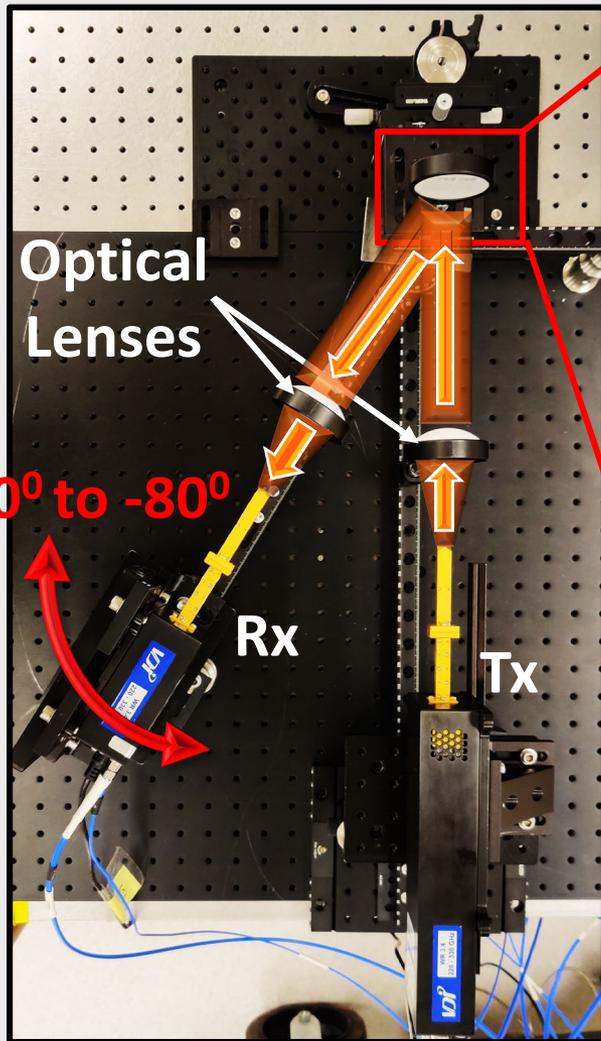


**Fabricated prototype of  $30 \times 30$  - 2D reflective surfaces on an alumina wafer**

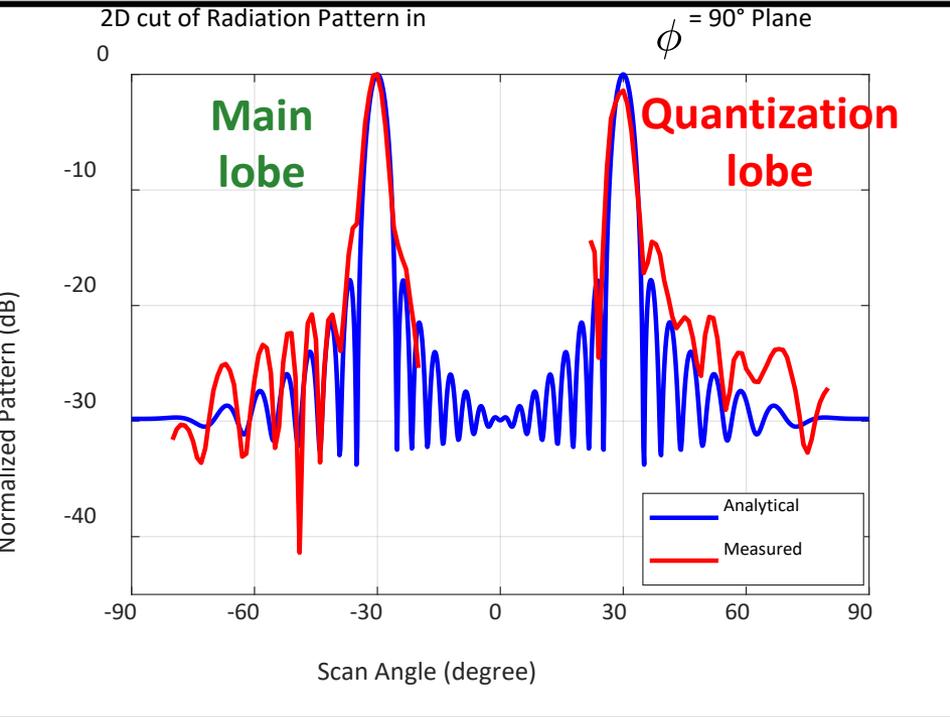
**High-mag image of individual unit cell**



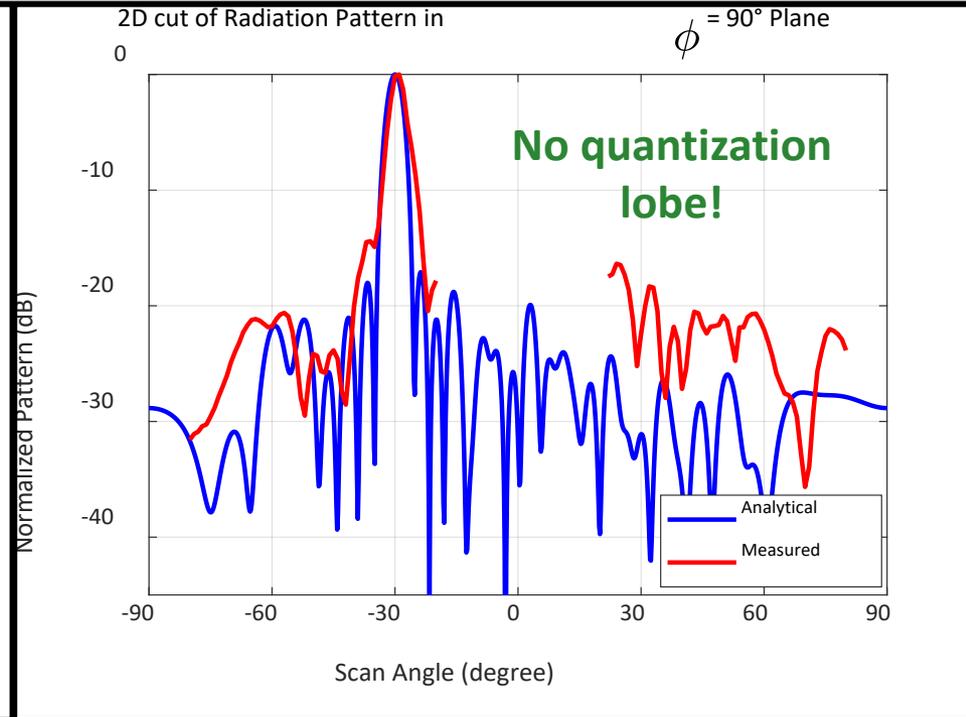
# Quasi-Optical Measurement Setup for RCS Characterization



## Non-Randomized Array

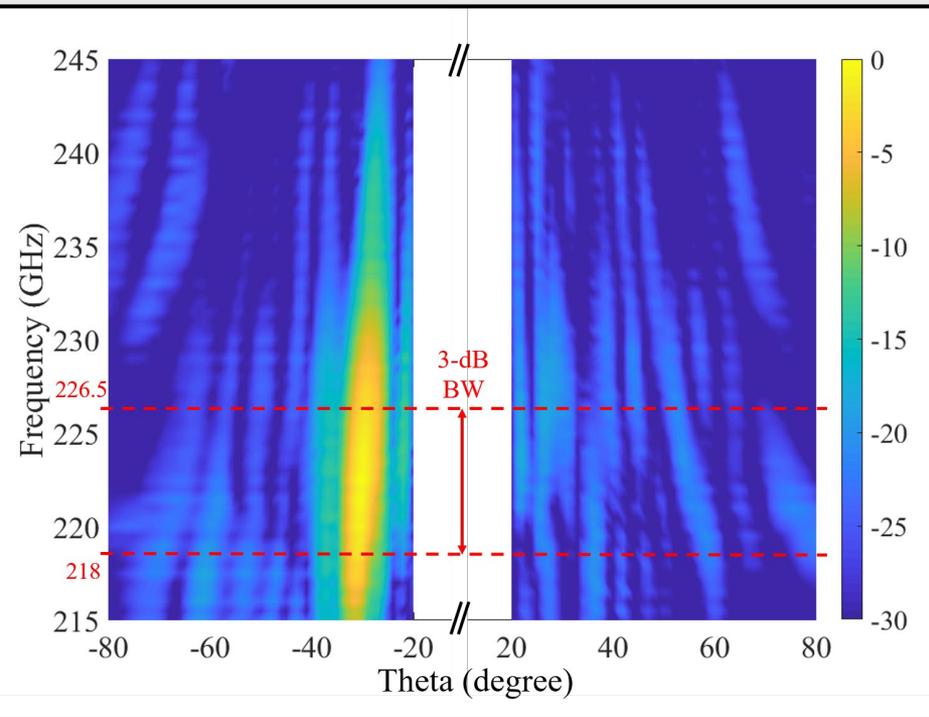
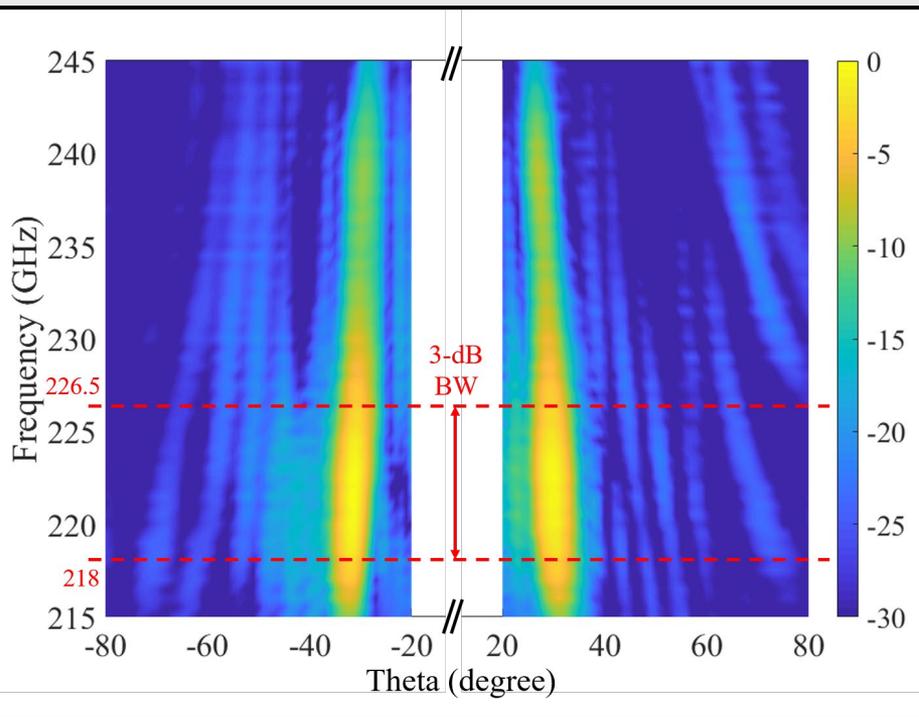


## Randomized Array

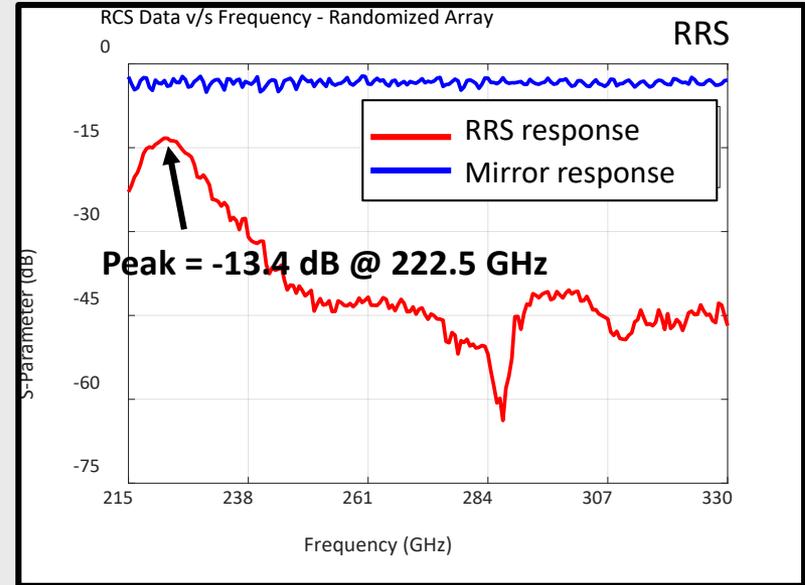
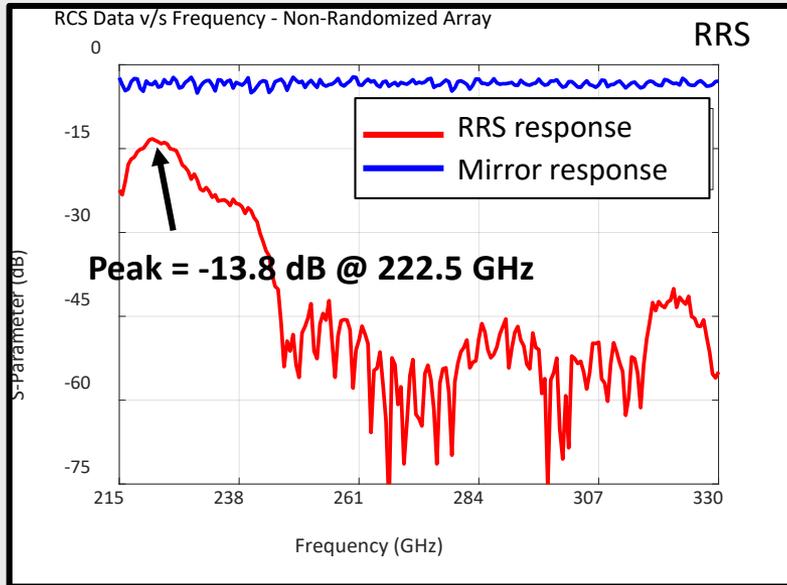


## Non-Randomized Array

## Randomized Array



**3dB BW = 8.5 GHz → Fractional BW of 3.8%**



## Inherent System Losses

**Reflective surface efficiency ( $\eta$ )  $\approx$  30 %**

## Designed RRS

Source of Loss	Magnitude of Loss
Alignment + system imperfections	-3.90 dB
Quantization Loss	-3.01 dB
Aperture Loss	-1.25 dB
<b>Total loss <math>\approx</math></b>	<b>-8.16 dB</b>

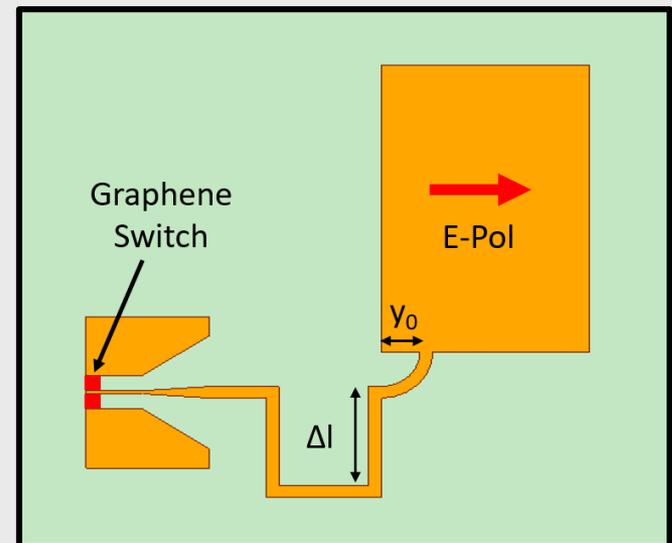
Source of Loss	Magnitude of Loss
Surface wave Loss	-1.80 dB
Dielectric Loss	-0.17 dB
Conductor Loss	-3.37 dB
<b>Total loss <math>\approx</math></b>	<b>-5.34 dB</b>

## Conclusions

- A robust technique for mitigating quantization lobes in single bit reconfigurable reflective surfaces with plane wave excitation is proposed.
  - Quantization lobe suppressed by randomizing the phase quantization error.
- Proposed a fabrication technique suitable for large scale production of mmWave /THz reflective surfaces.
- Characterized the RCS of mmWave reflective surfaces using a quasi-optical measurement setup.

## Future Work

- Incorporate actual switches to realize reconfigurability.



# Thank you!

E-mail: [Bharath.Kashyap@asu.edu](mailto:Bharath.Kashyap@asu.edu)